

FIG. 1

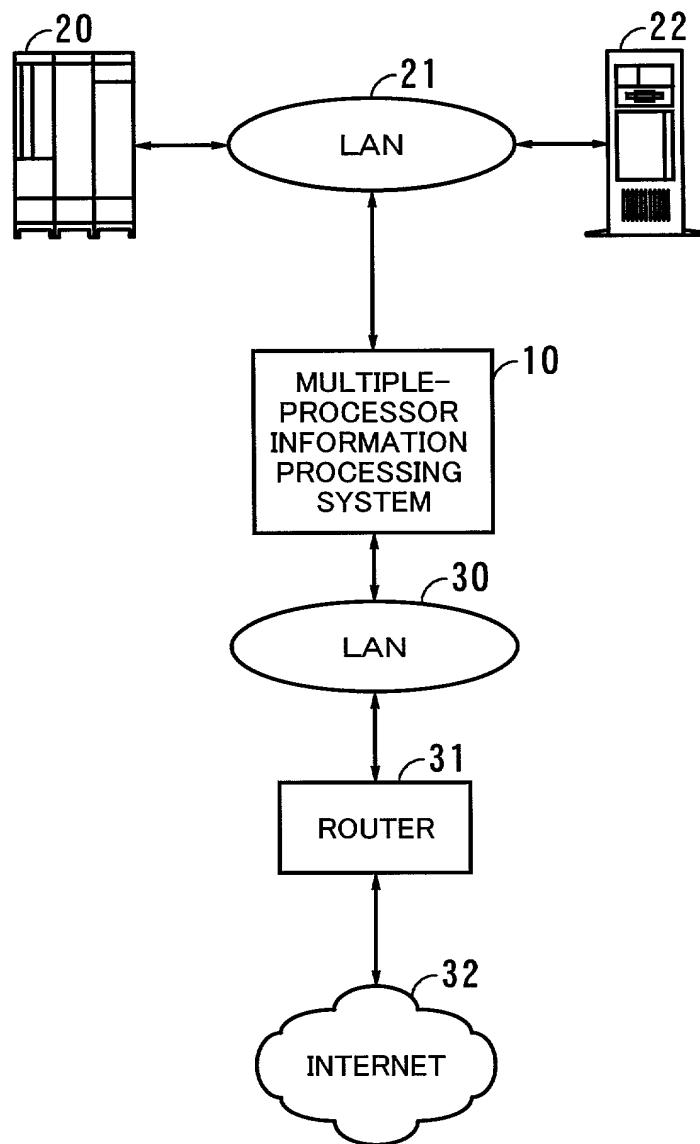


FIG. 2

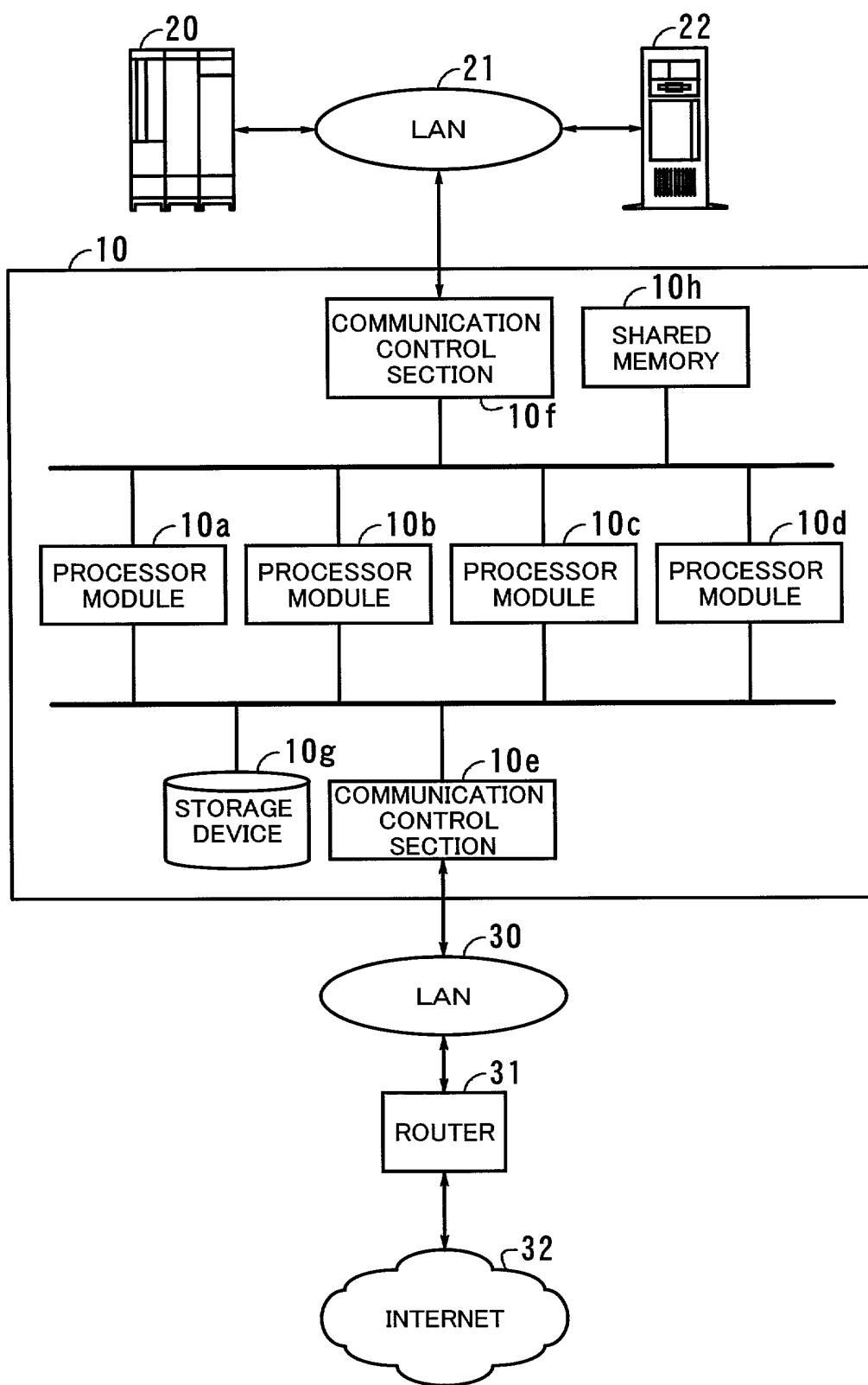


FIG. 3

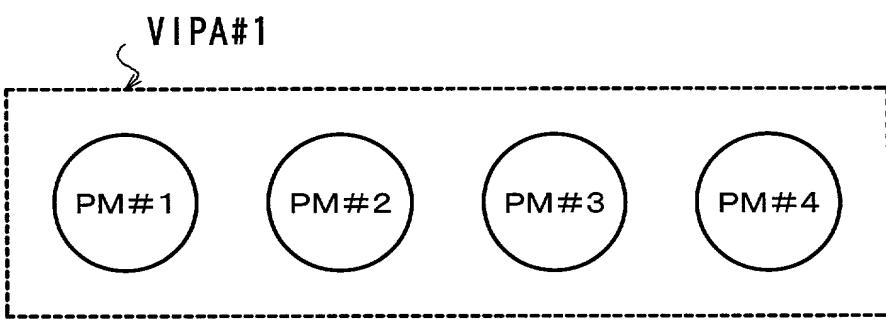


FIG. 4

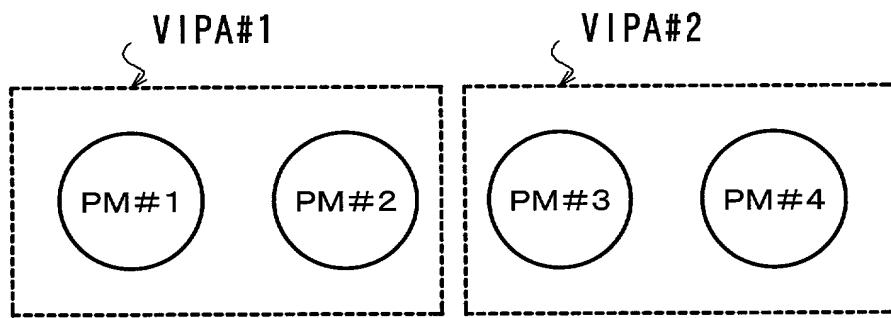


FIG. 5

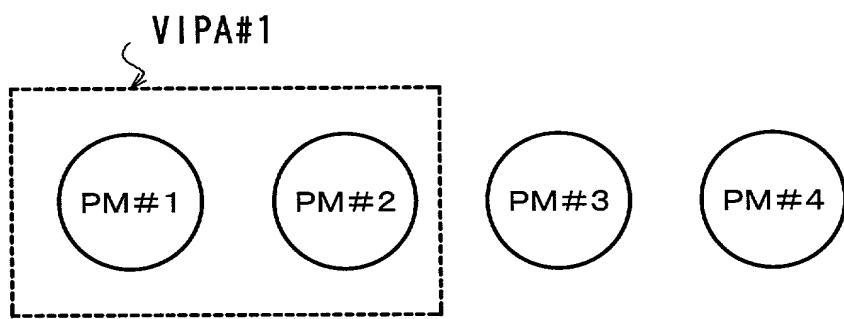


FIG. 6

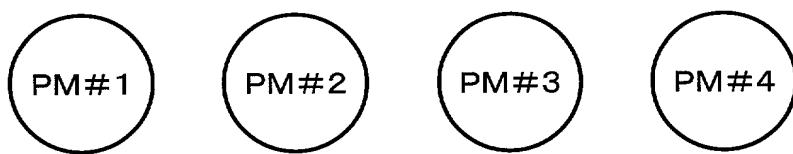


FIG. 7

| PROCESSOR MODULE | REAL IP ADDRESS | VIRTUAL IP ADDRESS | DESTINATION IP ADDRESS |
|------------------|-----------------|--------------------|------------------------|
| PM#1             | RIPA#1          | VIPA#1             | —                      |
| PM#2             | RIPA#2          | VIPA#1             | —                      |
| PM#3             | RIPA#3          | VIPA#2             | —                      |
| PM#4             | RIPA#4          | VIPA#2             | —                      |

FIG. 8

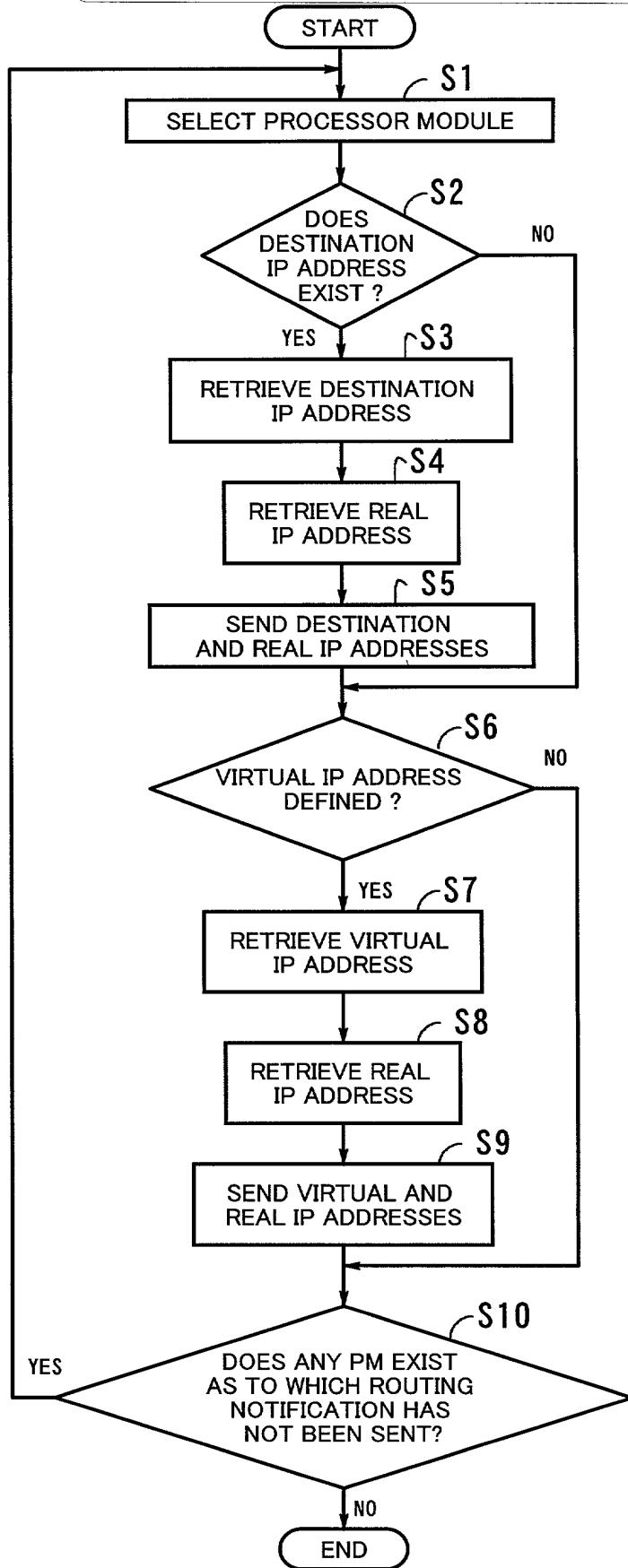


FIG. 9

| DESTINATION IP ADDRESS | GATEWAY ADDRESS | METRIC |
|------------------------|-----------------|--------|
| VIPA#1                 | RIPA#1          | 2      |
| VIPA#1                 | RIPA#2          | 2      |
| VIPA#2                 | RIPA#3          | 2      |
| VIPA#2                 | RIPA#4          | 2      |

FIG. 10